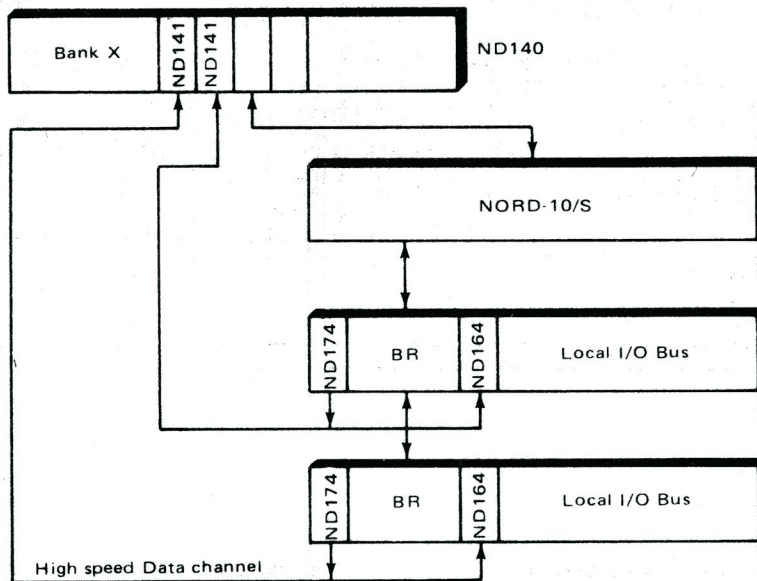


NORD
COMPUTER SYSTEMS



ND164 BUS BRANCHER

INTRODUCTION

The ND164 – BUS BRANCHER – is designed to be a high performance data channel which is connected to an independent memory port on the ND140 – MULTIPORT MEMORY SYSTEM. The BUS BRANCHER provides high speed data transfers to take place to memory – independently of the CPU.

FEATURES

- Separate high speed, 18 bit data channel with 256K words address space
- No cycle steal from CPU
- The BUS BRANCHER may be installed in any BUS RECEIVER (ND163) to provide a separate high speed data channel
- Parity checked transfers

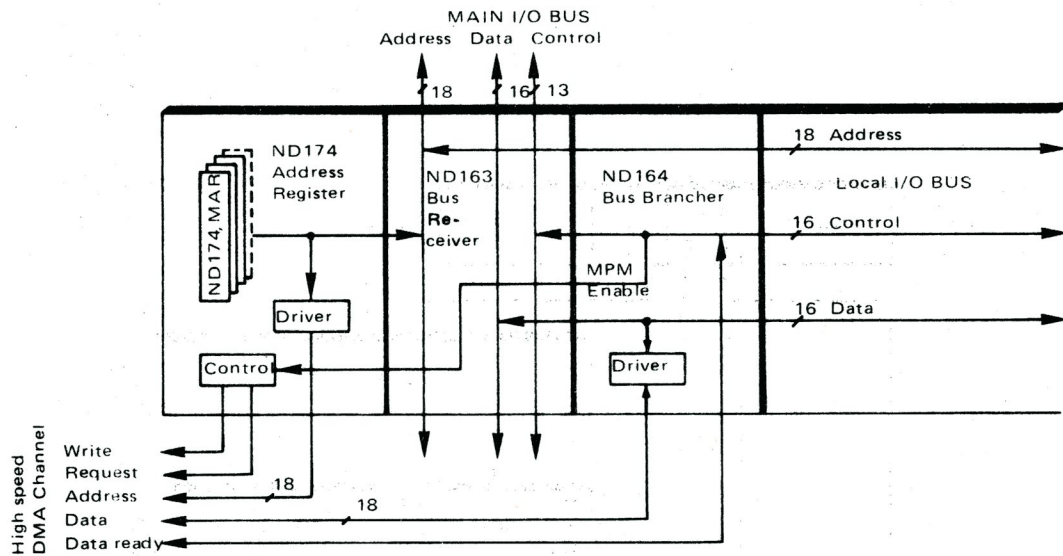
PRODUCT DESCRIPTION

Prerequisites for the BUS BRANCHER (ND164) are the ND163 – Bus Receiver, the ND174 – 16 Memory Address Registers, and a separate memory port – ND141 – in the Multiport Memory System.

When the BUS BRANCHER is installed in the available slot in the BUS RECEIVER, 18 bit data lines, 18 bit address lines and 3 control lines are available on separate connectors constituting the high speed data channel. The only cabling required are the two channel cables going to the separate memory port in the Multiport Memory System.

All transfers on the high speed data channel are parity checked. In event of parity error, the processor will be notified and two parity error indicators on the BUS BRANCHER (for least and most significant byte) will be activated.

164-B1-1800-0179



SPECIFICATIONS

Channel transfer rate: 1.6 Mbytes /second
 Parity: Parity per byte – odd parity
 Channel address space: 256 Kwords
 (16 bit words)

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NOTE: Norsk Data reserves the right to change specifications at any time without given notice!